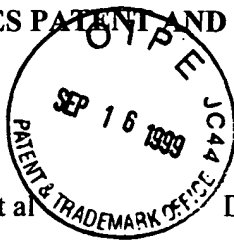


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



In re the application of:

Applicant : Alexander Kalnitsky, et al Docket No : 93-C-032C3  
Serial No : 09/007,668 Group : 2822  
Filed : January 15, 1998 Examiner : S. Meier  
For : Enhanced Planarization Technique for Batch No. : U34  
an Integrated Circuit

TRANSMITTAL LETTER

BOX: ISSUE FEE

Hon. Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Enclosed: (1) Issue Fee Transmittal PTOL-85B with Certificate of Mailing:  
(2) Check in the amount of \$1,240.00 for payment of Issue Fee and advanced order  
of ten (10) copies;  
(3) Our return postcard.

The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. 19-1353. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

Lisa K. Jorgenson  
Reg. No. 34,845  
Attorney for Applicants

STMicroelectronics, Inc.  
1310 Electronics Drive/MS 2346  
Carrollton, TX 75006  
972-466-7414

CERTIFICATE OF MAILING  
37 CFR 1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail with sufficient postage in an envelope addressed to: Box Issue Fee, Assistant Commissioner for Patents, Washington, D.C. 20231 on the date below:

Sept. 13, 1999  
Date

Angie Rodriguez  
Signature

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TRANSMITTAL OF FORMAL DRAWINGS

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Sir:

In response to the "Notice of Allowability" (PTOL-37) mailed June 14, 1999, in the above-referenced patent application, please find enclosed for filing four (4) sheet(s) of formal drawings.

I hereby authorize the Commissioner to charge any fees which may be required to Deposit Account No. 19-1353. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

*Lisa K. Jorgenson*  
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